Attorney Docket No.: CYPR-CD00184

# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

## **Patent Application**

Thereby certify that this transmittal of the below described document is being deposited with the United States Postal Service in an envelope bearing First Class Postage and addressed to the Assistant Commissioner for Patents, Washington, D.C., 20231, on the below date of deposit.  Date of 11/14/01 Name of Person  KATHERINE RINALDI Making the Deposit:  Washington D.C., 20231, on the below date of deposit.								
	11 4 /01	Name of Person Making the Deposit:	KATHERINE	RINALDI	Making the Deposit:	"Hitherile	pinace	
In to Application of:				٠.				

In re Application of:

Inventor(s): Craig Nemecek
Application No.: 09/975,105

Filed: 10/10/01

Title: HOST TO FPGA INTERFACE IN AN IN-CIRCUIT EMULATION SYSTEM

Assistant Commissioner for Patents Washington, D.C. 20231

CONDITIONAL PETITION FOR EXTENSION OF TIME

This conditional petition is being filed concurrently with the filing of a <u>CONTINUATION-IN-PART</u> <u>PATENT APPLICATION</u>

and provides for the possibility that applicant has inadvertently overlooked the need for a petition and fee for extension of time.

# C nditional petition for extension of time

If any extension of time for the accompanying response is required, applicant requests that this be considered a petition therefor.

#### Status

This ap	plication is on behalf of
	x other than a small entity a small entity
	A verified statement:
	is attached is already filed
[ x ]	The Commissioner is hereby authorized to charge any additional fees associated with this communication or credit any overpayment to Deposit Account No.: 23-0085.  A duplicate copy of this authorization is enclosed.



Please direct all correspondence concerning the above-identified application to the following address:

### WAGNER, MURABITO & HAO LLP

Two North Market Street, Third Floor San Jose, California 95113 (408) 938-9060

Respectfully submitted,

Date: U(M/M

Glenn D. Barnes

Reg. No. 42,293

Notification to Parent of CIP Application Filing
Applicant: C. Nemecek
Docket No.: cypr-cD00184
S/N.:09/975,105
Title: HOST TO FPGA INTERFACE IN AN IN-CIRCUIT
EMULATION SYSTEM

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Please acknowledge receipt of the following:

A. Certificate of Mailing

A. Conditional Petition for an Extension of Time

A Notification of Filing of CIP

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